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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Brintzinger, *et al.* Docket No.: 2003 P 51167 US
Serial No.: 10/826,603 Art Unit: 2812
Filed: April 16, 2004 Examiner: TBD
For: Method for Protecting the Redistribution Layer on Wafers/Chips

Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))

Date of Deposit: August 9, 2004

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Certificate of Mailing via First Class Mail (1 page)
Information Disclosure Statement (1 page)
Form PTO/SB/08a (1page) citing 7 references, 3 included herewith
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Respectfully submitted,


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Mail Stop: Amendment
Commissioner for Patents
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INFORMATION DISCLOSURE STATEMENT

Dear Sir:

The Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application. Pursuant to 37 CFR 1.98(a)(2)(i), as amended, copies of U.S. Patents cited are not being submitted. However, Applicant has included copies of the foreign patents.

No fee is due at this time, as this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b)(3), before the mailing of a first Office action on the merits.

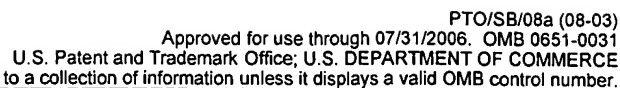
Respectfully submitted,

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August 9, 2004

Date

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Application Number	10/826,603
Filing Date	April 16, 2004
First Named Inventor	Brintzinger, <i>et al.</i>
Art Unit	2812
Examiner Name	TBD
Attorney Docket Number	2003 P 51167 US

Sheet	1	of	1
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